3ʀᴅ International Conference on

MICROELECTRONICS, COMPUTING SYSTEMS, MACHINE LEARNING & INTERNET OF THINGS (MCMI-2022)

INDIAN SOCIETY FOR VLSI EDUCATION

RANCHI-835217 (JHARKHAND) INDIA

Best Paper Award Certificate

Dr. Jai Prakash Gupta Memorial Award in Microwave Engineering

Technical Session: Microelectronics & Computing Systems

This is to certify that Huirem Bharat Meitei and Manoj Kumar

of National Institute of Technology, Manipur, Langol India, Pin 705004

has/have been participated and presented a paper title FPGA IMPLEMENTATION OF TRNG ARCHITECTURE USING ADPLL BASED ON GATED CLOCK LOOP FILTER in the 3ʳᵈ *International Conference on Microelectronics, Computing Systems, Machine Learning & Internet of Things (MCMI-2022)* organized by ISVE Ranchi at **Advanced Regional Telecom Training Centre**, **BSNL,** Hazaribag Road, Ranchi-835217, Jharkhand, India *during Sept. 17 -18ᵗʰ , 2022 in* **Virtual Mode.**

Dr. Vijay Nath	**Dr. Anand K. Thakur**	**Dr. Raj Kumar Singh**	**Dr. D.K. Yadav**
General Chair_MCMI-2022	Organizing Secretary_MCMI-2022	Convener_MCMI-2022	President ISVE Ranchi
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