



*Department of Electrical Engineering*  
राष्ट्रीय प्रौद्योगिकी संस्थान, मणिपुर  
**NATIONAL INSTITUTE OF TECHNOLOGY, MANIPUR**  
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An Autonomous Institute under MHRD, Govt. of India.

Ref. No. NITM.3/(90-Acad)/EE-Notice/2017/46

Date: 10/01/2020

The following candidates have been shortlisted (provisionally) for appearing for an Interview before the selection committee for admission to PhD program in the Electrical Engineering Department, NIT Manipur for the session January, 2020.

Sl.No	Name Of The Candidate	Category	GATE/NET	Mode Selection of	Remarks
1	Milner Paul V	Part time	GATE	Interview only	Original NOC has to be submitted

**Important Date & Time**

Date of reporting: **17 January 2020**

Time: **11:00 am.**

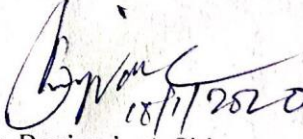
Venue: **Office of the HOD, Electrical Engineering Department, NIT Langol**

Date of Examination/interview: **17 January, 2020**

Time: **11:30 am** onward

**Note:**

1. Candidates have to produce all original mark sheets, certificates/ testimonials/ valid GATE/NET score card for verification at the time of interview.
2. Part time candidate has to produce NOC (in Original) at the time of Interview
3. No TA/DA shall be admissible to the candidates for appearing for the interview.

  
18/1/2020  
Dr. Benjamin A Shimray  
HOD, EE