



Ref. No. NITMNECE/2019/31

Dated: 15/01/2020

NOTICE

It is hereby notified that the following candidates are shortlisted:

- (i) For written test for admission to PhD programme in ECE Department in January 2020 session


SL	NAME	CATEGORY	GATE/NON GATE	PART TIME/FULL TIME
1	OINAM SUCHITRA DEVI	OBC	NON GATE	FULL TIME
2	MOICHINGMAYUM PILOT KHAN	OBC	NON GATE	FULL TIME
3	NINGTHOUKHONGJAM KUNAN	GENERAL	NON GATE	FULL TIME
4	HUIREM BHARAT MEITEI	OBC	NON GATE	FULL TIME
5	RAMKHUN GANGMEI	ST	NON GATE	FULL TIME

- (ii) For direct interview for admission to PhD programme in ECE Department in January 2020 session

SL	NAME	CATEGORY	GATE/NON GATE	PART TIME/FULL TIME
1	ROSY KIMNEIthem HAOKIP	ST	GATE	PART TIME (NOC)
2	SARUNGBAM SHILA	OBC	GATE	FULL TIME
3	MOHD AZHARUDDIN	OBC	GATE/NET	FULL TIME

The Written test will be held on 21/01/2020 10am (Reporting Time 9.30am) at VLSI Lab Langol Campus.

Candidates appearing for written test and who have subsequently passed the written test will have to appear for interview along with the GATE/NET qualified students, which will be held on 21/01/2020 2pm (Reporting Time 1.30pm) at VLSI Lab Langol Campus


(Dr. L. Surajkumar Singh)
Head of Department

Copy to:

1. Director, for kind information
2. Dean Academic, for kind information.
- ✓ System Administrator, for uploading to Institute website for general information
4. Relevant file.