



**Department of Electronics & Communication Engineering**

राष्ट्रीय प्रौद्योगिकी संस्थान, मणिपुर

**NATIONAL INSTITUTE OF TECHNOLOGY MANIPUR**

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(An Autonomous Institute under MHRD, Govt. of India)

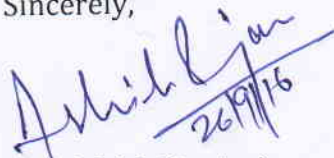
Dear Sir/ Madam,

It gives me immense pleasure to inform you that the Department of Electronics and Communication Engineering, National Institute of Technology Manipur, in conjunction with Electronics and ICT Academy (E&ICT -IIT Guwahati), is organizing a three days workshop on **“Recent Trends in VLSI Design”** from 3<sup>rd</sup> October 2016 to 5<sup>th</sup> October 2016. This course will offer a unique opportunity to the faculty, researchers, engineers and research students working in the relevant topics of Analog VLSI Design through interactive lectures and hands on sessions. Expert speakers will be from ISRO, IITs, NITs and Industry. We take this opportunity to extend our invitation to you, your faculty/ colleague, research scholars and students to be part of this course. No registration fee is required. Interested participants need to register for the workshop in the link provided at [http:// www.nitmanipur.ac.in](http://www.nitmanipur.ac.in)

Also please find the brochure for the workshop enclosed. Kindly display it in your institution/ department. Please do not hesitate to contact us for any further information.

Thanking you,

Sincerely,

  
26/9/16  
(Dr. Ashish Ranjan)

Programme Coordinator

Department of Electronics and Communication Engg.,

National Institute of Technology Manipur

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